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[Correction of a Memory Management Method for Lock-Free Data... - Michael, Scott \(1995\)](#) [\(Correct\)](#) [\(5 citations\)](#)

The method uses four basic routines: NEW, RECLAIM, SAFEREAD, and RELEASE. NEW allocates a node

Correction of a **Memory** Management Method for LockFree Data Structures

structures. **2 Memory** Management Method In this **section** we present an overview of Valois's **memory**

[hylaia.dcs.qmw.ac.uk/data/edu/cs.rochester.edu/systems/95.tr599.Memory\\_management\\_for\\_lock-free\\_data\\_structures.ps.gz](http://hylaia.dcs.qmw.ac.uk/data/edu/cs.rochester.edu/systems/95.tr599.Memory_management_for_lock-free_data_structures.ps.gz)

[Control of the QUENCH Protection System at HERA - Bacher Duval](#) [\(Correct\)](#)

the lower level quench microprocessor and PLC based **alarm** control center is connected via the CAN fieldbus.

A PLCbased microprocessor functions as an **alarm** control center collecting **alarm** signals, such as

functions as an **alarm** control center collecting **alarm** signals, such as quench electronics status, power

[adwww.fnai.gov/www/fcalepcs/abstracts/Postscript/fpo3.ps](http://adwww.fnai.gov/www/fcalepcs/abstracts/Postscript/fpo3.ps)

[Uniprocessor Garbage Collection Techniques - Wilson \(1992\)](#) [\(Correct\)](#) [\(212 citations\)](#)

While in many systems programmers must explicitly **reclaim** heap **memory** at some point in the program, by

systems programmers must explicitly **reclaim** heap **memory** at some point in the program, by using a 'free'

[inge-ns.ing.uniroma1.it/~nardi/LT9899/dispense/gcsurvey.ps](http://inge-ns.ing.uniroma1.it/~nardi/LT9899/dispense/gcsurvey.ps)

[Mechanisms and Interfaces for Software-Extended Coherent Shared... - Chaiken \(1994\)](#) [\(Correct\)](#) [\(3 citations\)](#)

Interfaces for SoftwareExtended Coherent Shared **Memory** by David L. Chaiken Sc.B.Brown University

in this document that I did not write, it is in **Sections** 2.1.3 and 3.2.2. **Section** 2.1.3 describes the

[ftp.ccs.mit.edu/pub/papers/chaiken-dissert-1-10.ps.Z](http://ftp.ccs.mit.edu/pub/papers/chaiken-dissert-1-10.ps.Z)

[Declaration - Include Ilupre](#) [\(Correct\)](#)

from the matrix A. DiagPreconditioner (void )**Reclaim** **memory** space. Member Functions Vector double

matrix A. DiagPreconditioner (void )**Reclaim** **memory** space. Member Functions Vector double solve (

hiding such issues, we have included this **section** to assist the user in integrating SparseLib

[math.nist.gov/pub/pozo/docs/sparselib.ps.gz](http://math.nist.gov/pub/pozo/docs/sparselib.ps.gz)

[Working Memory and Dyslexia - Fawcett, Baddeley \(1992\)](#) [\(Correct\)](#)

5/19/95 1 **Working Memory** and Dyslexia Roderick I. Nicolson\*Angela J.

groups. Fortunately, as described in the next **section** we were able to select two groups of dyslexic

[ftp.shef.ac.uk/pub/uni/projects/scp/lrgdocs/lrg913.ps](http://ftp.shef.ac.uk/pub/uni/projects/scp/lrgdocs/lrg913.ps)

[Using Neural Networks for Alarm Correlation in... - Wietgreffe, Tuchs... \(1997\)](#) [\(Correct\)](#) [\(4 citations\)](#)

training time, calculation time during runtime and **memory** requirements. To model and train the Cascade

operators will not be confused. As discussed in **section** 7.2.97, Seite 8 two, the CCAC can easily be

7.2.97, Seite 1 Using Neural Networks for **Alarm** Correlation in Cellular Phone Networks Hermann

[www.kbs.uni-hannover.de/paper/97/iwan.ps](http://www.kbs.uni-hannover.de/paper/97/iwan.ps)

[A Data Mining Methodology and Its Application to... - Klemettinen...](#) [\(Correct\)](#)

The rest of this paper is organized as follows. In **Section** 2 we briefly discuss two motivating ex amples

which discovers patterns in telecommunication **alarm** databases. In this paper, we give concrete ex

to use frequent patterns in the construc tion of **alarm** correlation expert systems. 1 Introduction Data

[www.cs.helsinki.fi/research/fdk/datamining/pubs/dexa97.ps.gz](http://www.cs.helsinki.fi/research/fdk/datamining/pubs/dexa97.ps.gz)

[ANSWER: Network Monitoring Using Object-Oriented Rules - Gary Weiss \(1998\)](#) [\(Correct\)](#) [\(2 citations\)](#)

component. Payoff and Benefits We begin this **section** by describing the most visible benefits of

4ESS switches and processes over 100,000 4ESS **alarms** per week. Introduction Network reliability is of

an anomalous event, a 4ESS switch will generate an **alarm** and send it to one of AT&T's two technical

[www.research.att.com/sw/tools/r%2B%2B/iaai98.ps](http://www.research.att.com/sw/tools/r%2B%2B/iaai98.ps)

[Integrating Industrial Control Systems Into The Control... - Sollander Blanc](#) [\(Correct\)](#)

electricity distribution and safety. The TCR is an **alarm** driven control room in the sense that the

control room in the sense that the arrival of an **alarm** will alert the operator and make him take appropriate actions. The operator acts upon the **alarms** primarily by consulting and interacting with  
[adwww.fnal.gov/www/icalpcps/abstracts/Postscript/wpo67.ps](http://adwww.fnal.gov/www/icalpcps/abstracts/Postscript/wpo67.ps)

Relational Bayesian Networks - Jaeger (1997) (Correct) (14 citations)

in terms of  $r(d \ 2 \ d \ 1)$  and vice versa. As in **section 3**, for every  $r \ 0 \ 2 \ Pa(r)$  frg a formula  $pa \ rr \ 0$   
 A network with r.v.s (earth)quake, burglary, and **alarm**, each with possible values frue, falseg, for the network. If, for instance,  $E = fquake = true$ , **alarm** = trueg, then both instantiations are assumed to  
[l2r.cs.uiuc.edu/~danr/Other-papers/Topics/Reasoning/Bayes/relational-bayes.ps.gz](http://l2r.cs.uiuc.edu/~danr/Other-papers/Topics/Reasoning/Bayes/relational-bayes.ps.gz)

Integrating Temporal, Real-Time, and Active Databases - Ramamritham.. (1996) (Correct) (3 citations)

for data placement at the appropriate level of **memory** hierarchy, for avoiding undoing/redoing by  
[www-ccs.cs.umass.edu/~sim/sigrec96.ps](http://www-ccs.cs.umass.edu/~sim/sigrec96.ps)

A Prefetching IPC Mechanism for Low-Latency Transfer of... - Hajime Miyazawa (Correct)

The IPC mechanism uses three key techniques: **memory** mapping, typed prefetching and controlling the respectively ship and acquire a designated **memory section** corresponding to an element of structured data  
[www-masuda.is.s.u-tokyo.ac.jp/publications/miyazawa-icdp96.ps.gz](http://www-masuda.is.s.u-tokyo.ac.jp/publications/miyazawa-icdp96.ps.gz)

Motivation-Based Direction of Planning Attention in Agents.. - James, Norman (1997) (Correct) (3 citations)

in the present context i.e. an associative **memory** approach to the action selection problem, cf. effort, and possibly less physical effort (see **section 5.1**) The **alarm** processing machinery pre sented attention in agents with goal autonomy. These '**alarm** processing' mechanisms serve to focus the  
[www2.elec.qmw.ac.uk/~tin/thesis/thesis.ps.gz](http://www2.elec.qmw.ac.uk/~tin/thesis/thesis.ps.gz)

Multiple alarms, Major Goals and Implementation. - Lublinsky, Fermi (Correct)

Multiple **alarms**, Major Goals and Implementation. B. Lublinsky.  
 to recognize a regime change and reconfigure its **alarm** system accordingly. For example, when a magnet the cryogenic control system will generate many **alarms**. Only a few of them are important and they may  
[adwww.fnal.gov/www/icalpcps/abstracts/Postscript/wpo24.ps](http://adwww.fnal.gov/www/icalpcps/abstracts/Postscript/wpo24.ps)

Optimized Software Synthesis for Digital Signal.. - Jürgen Teich.. (1998) (Correct) (1 citation)

.5 2 An Evolutionary Approach for **Memory** Optimization 7 2.1 The SDF scheduling framework . actor orderings [MBL94] discussed further in **Section 2.3.2** for constructing buffer **memory** optimal  
[ftp.tik.ee.ethz.ch/pub/people/zitzler/TZB1998a.ps.gz](http://ftp.tik.ee.ethz.ch/pub/people/zitzler/TZB1998a.ps.gz)

Schemes for Fault Identification in Communication Networks - Irene Katzela (1995) (Correct) (20 citations)

schemes. The work is organized as follows: In **section 2** we define the problems of fault identification Based on that model we design an algorithm for **alarm** correlation and fault localization and analyze process can be divided into three stages, **alarm** correlation, fault identifica tion, and the  
[www.comm.toronto.edu/~irene/papers/ctr.ps.gz](http://www.comm.toronto.edu/~irene/papers/ctr.ps.gz)

The Control System Database for the DØ Detector - Laura Paterno (Correct)

or leaves the **alarm** conditions. In the following **sections** we discuss the organization of the Hdb database, local databases contain the access information and **alarm** conditions for all the devices that the monitor and control. The processors use the **alarm** information to monitor the hardware for failures  
[adwww.fnal.gov/www/icalpcps/abstracts/Postscript/wpo30.ps](http://adwww.fnal.gov/www/icalpcps/abstracts/Postscript/wpo30.ps)

The Case For Reliable Concurrent Multicasting Using.. - Levine, Lavo.. (1996) (Correct) (32 citations)

re transmitting packets can delete packets from **memory** within a finite time. The development and We establish our case in three parts. First, in **Section 2**, we summarize the known classes of protocols  
[www.cse.ucsc.edu/research/corg/publications/brian.mm96.ps.gz](http://www.cse.ucsc.edu/research/corg/publications/brian.mm96.ps.gz)

Garbage Collection Based on a Linear Type System - Igarashi, Kobayashi (2000) (Correct) (4 citations)

management: **memory** space for linear values can be reclaimed immediately after they are used. However, (GC) scheme for a programming language with static **memory** management based on a linear type system. Linear example, the elements in p above are linear. See **Section 5** for discussion. 1.3 Our Contribution The  
[reports-archive.adm.cs.cmu.edu/80/anon/2000/CMU-CS-00-161F.ps](http://reports-archive.adm.cs.cmu.edu/80/anon/2000/CMU-CS-00-161F.ps)

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[The Technical Data Server For The Control Of 100 000 Points Of ... - Ninin Laeger](#) (Correct)

Specification Minimum hardware CPU All current WS **Memory** All current WS Platform All current WS Version interfaces will be described in the following **sections**. A. Equipment control systems The Technical Data the interfaces to equipment and to existing **alarm** and data logging systems and to operator  
[adwww.fnai.gov/www/calepcs/abstracts/Postscript/w1ae.ps](http://adwww.fnai.gov/www/calepcs/abstracts/Postscript/w1ae.ps)

[Knowledge Discovery from Telecommunication Network.. - Hätönen.. \(1996\)](#) (Correct) (23 citations)

a PC with 90 MHz Pentium processor and 16 MB main **memory**, under the Linux operating system. The **alarm** data in the telecommunication network **alarm** databases. **Section 3** points out what types of data cleaning Knowledge Discovery from Telecommunication Network **Alarm** Databases K. Hatonen M. Klemettinen H. Mannila  
[www.cs.helsinki.fi/research/pmdm/datamining/pubs/icde96.ps.gz](http://www.cs.helsinki.fi/research/pmdm/datamining/pubs/icde96.ps.gz)

[Software Transactional Memory - Shavit \(1995\)](#) (Correct) (36 citations)

Software Transactional **Memory** Nir Shavit (Lambda MIT and TelAviv University  
 concurrent objects by means of critical **sections** are unsuitable, since they limit parallelism,  
[wilma.cs.brown.edu/courses/cs295h/stm.ps](http://wilma.cs.brown.edu/courses/cs295h/stm.ps)

[Points Deviations - A pattern language for fire alarm systems - Peter Molin](#) (Correct) (7 citations)

optimization and optimal utilization of limited **memory**. While these issues are still important as the in the classic pattern format with three **sections**: context, problem and solution. Following the 1 Points & Deviations A pattern language for fire **alarm** systems Peter Molin and Lennart Ohlsson  
[www.cs.wustl.edu/~schmidt/PLoP-96/molin.ps.gz](http://www.cs.wustl.edu/~schmidt/PLoP-96/molin.ps.gz)

[Distributed Cyclic Reference Counting - Dehne, Lins \(1994\)](#) (Correct)

jects being transferred, and it may be unable to **reclaim** large cyclic structures that span over several processors. 1 Introduction In distributed **memory** multiprocessors, each processor is responsible of this paper is organized as follows. In **Section 2** we present our algorithm and in **Section 3** we  
[www.scs.carleton.ca/publications/tech\\_reports/1994/TR235.ps](http://www.scs.carleton.ca/publications/tech_reports/1994/TR235.ps)

[Autoscheduling in a Distributed Shared-Memory Environment - Jos'e Moreira \(1994\)](#) (Correct) (7 citations)

Autoscheduling in a Distributed SharedMemory Environment (Lambda Jos'e E. Moreira Constantine architecture. This paper is organized as follows: **Section 2** describes our target machine architecture and  
[ftp.csrd.uiuc.edu/pub/CSRD\\_Reports/reports/1373.ps.gz](http://ftp.csrd.uiuc.edu/pub/CSRD_Reports/reports/1373.ps.gz)

[Effective Compiler Support for Predicated Execution .. - Mahlke, Lin, Chen, .. \(1992\)](#) (Correct) (109 citations)

paths with subroutine calls or unresolvable **memory** accesses can restrict optimization and scheduling The remainder of this paper consists of four **sections**. In **Section 2**, the architecture support we  
[cardiff.et.tudelft.nl/~steven/ftp/mahlke92.ps.gz](http://cardiff.et.tudelft.nl/~steven/ftp/mahlke92.ps.gz)

[A Case Study in the Qualitative Verification and Debugging.. - Parsons, Saffioti \(1993\)](#) (Correct) (1 citation)

structure of the rest of the paper is as follows. **Section 2** describes the problem which we are using in circuit breaker isolates this line and transmits an **alarm** to the control room. The **alarm** may be either an and transmits an **alarm** to the control room. The **alarm** may be either an instantaneous **alarm** or a delayed  
[tintin.oru.se/pub/saffioti/uncertainty/ijar96.ps.gz](http://tintin.oru.se/pub/saffioti/uncertainty/ijar96.ps.gz)

[A Cyclic Distributed Garbage Collector for Network Objects - Helena Rodrigues \(1996\)](#) (Correct) (7 citations)

groups, according to appropriate heuristics, to **reclaim** distributed garbage cycles. The algorithm 18, 19, 17, 9, 22] motivated by the complexity of **memory** management and the desire for transparent object reclamation. The paper is organised as follows. **Section 2** briefly describes the overall design of the  
[para.inria.fr/~iefessan/dgc/papers/00050-RJ96.ps.gz](http://para.inria.fr/~iefessan/dgc/papers/00050-RJ96.ps.gz)

[Applying the Object-Oriented Framework Technique to a Family of.. - Molin](#) (Correct)

terms of the capacity of the system, the amount of **memory** available, or the CPU processing speed. The suitable for our goals. The paper starts with a **section** giving a brief overview of the product domain.

an object oriented framework for a family of fire alarm system products. TeleLarm AB, a Swedish security  
 bilbo.ide.hk-r.se:8080/~pino/papers/exp5.ps

Computing Global Virtual Time in SharedMemory Multiprocessors - Richard Fujimoto And (2001) (Correct) (4 citations)  
 perform irrevocable operations such as I/O and to **reclaim** storage. Most existing algorithms for computing  
 computation is examined in the context of a shared-memory model. We observe that computation of GVT is much  
 Procedure to initiate a GVT computation (critical section) prevent multiple PEs from setting flag \*  
 www.cs.rpi.edu/~chrisc/.COURSES/PADS/FALL-2001/PAPERS/p425-fujimoto.pdf

Effectiveness of Garbage Collection and Explicit Deallocation - Hirzel (2000) (Correct)  
 live objects. This means that they may fail to **reclaim memory**, even though it is only reachable  
 if those are not aligned or point into a **memory** range known not to contain live objects. This  
 before and after instrumentation. The rest of this **section** describes the implementation of the  
 csel.cs.colorado.edu/~hirzel/misc/ms\_thesis.ps

Efficient Support for P-HTTP in Cluster-Based Web Servers - Aron, Druschel, Zwaenepoel (1999) (Correct) (10 citations)  
 due to improved hit rates in the backend's main **memory** caches, 2) increased secondary storage  
 The rest of the paper is organized as follows. **Section 2** provides some background information on  
 www.cs.rice.edu/~aron/papers/phttp-lard.ps

Array SSA for Explicitly Parallel Programs - Collard (1998) (Correct) (2 citations)  
 for parallel programs with either weak or strong **memory** consistency, with eventbased syn chronization  
 chronization or mutual exclusion, with parallel **sections** or indexed parallel constructs. 1 Introduction  
 www.prism.uvsq.fr/rapports/1998/document\_1998\_47.ps.gz

A Unifying Type-Theoretic Framework for Objects - Hofmann, Pierce (1993) (Correct) (24 citations)  
 message passing in a typetheoretic setting. In **Sections 2** and **3**, we introduce the basic constructions of  
 www.cs.indiana.edu/pub/pierce/abstrop.ps.gz

Performance Evaluation and Modeling of MPI Communications . - Folino, Spezzano, Talia (Correct)  
 The CS2 (Computing Surface 2) is a distributed **memory** MIMD parallel computer. It consists of Sparc  
 switches [4]The CS2 network provides a bisectional bandwidth that scales linearly in the number  
 isi-cnr.deis.unical.it:1080/~talia/hpcn98.ps

Location Consistency: Stepping Beyond the Barriers of Memory . - Gao, Sarkar (1994) (Correct) (8 citations)  
 Consistency: Stepping Beyond the Barriers of **Memory** Coherence and Serializability Guang R. Gao Vivek  
 jazz.snu.ac.kr/~joonwon/dsm/paper/062\_LocationConsistency\_SteppingBeyondTheMemoryCoherenceBarrier\_memo\_ICPP

Lightweight Transactions on Networks of Workstations - Athanasios Papathanasiou (1998) (Correct)  
 original database and space from the redo log is **reclaimed**. PERSEAS eliminates the redo log file, used in  
 present PERSEAS ,a transaction library for main **memory** databases that decouples the performance of  
 www.ics.forth.gr/arch-visi/OS/papers/1998.ICDCS.ps.gz

Low Latency Word Serial CORDIC - Villalba, Lang (1997) (Correct)  
 for both modes, as presented in this paper. In **Section 2** we reduce the number of iterations by merging  
 ftp.ac.uma.es/pub/reports/1997/UMA-DAC-97-05.ps.gz

Change Detection Design For Low False Alarm Rates - Gustafsson, Palmqvist (Correct)  
 the navigation system in aircrafts, detailed in **Section 3**. The false **alarm** rate should here be one in  
 Change Detection Design For Low False **Alarm** Rates Fredrik Gustafsson And Jan Palmqvist  
 tuning of change detectors with given false **alarm** rate. By estimating a parametric distribution to  
 ankeborg.isy.liu.se/~fredrik/reports/safe97cdtuning.ps

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